

## TIME TABLE

2.10.2018

<http://ii.pw.edu.pl/kowalski/dsp/edspa>[kowalski@ii.pw.edu.pl](mailto:kowalski@ii.pw.edu.pl)

2018/2019	October					November					December					January				February	
Monday	1	8	15	22	29	5	12	19	26	3	10	17	24	31	7	14	21	28	4	11	
Tuesday	2	9	16	23	30	6	13	20	27	4	11	18	25	1	8	15	22	29	5	12	
Wednesday	3	10	17	24	31	7	14	21	28	5	12	19	26	2 <sup>Pon</sup>	9	16	23	30	6	13	
Thursday	4	11	18	25	1	8	15	22	29	6	13	20	27	3	10	17	24	31	7	14	
Friday	5	12	19	26	2	9	16	23	30	7	14	21	28	4	11	18	25	1	8	15	
Saturday	6	13	20	27	3	10	17	24	1	8	15	22	29	5	12	19	26	2	9	16	
Sunday	7	14	21	28	4	11	18	25	2	9	16	23	30	6	13	20	27	3	10	17	

Lesson	Lab	Lab	
Tue 14.15- 16.00 r. 168	T1 Tue 16.15- 20.00 r. 9B	W1 Wed 16.15- 20.00 r. 9B	
2.10			Introduction, DSP Memory Architecture, Harvard architecture, C28x Memory Spaces,
9.10			Traditional DSP (multimedia),
16.10			F28x Architecture, CPU core, CLA, MCU, FP, Oscillators, Watchdog, Reset, Boot,
23.10			F28x Interrupts, CPU Timer, PIE, GPIO, ePWM (TB, CC, AQ), ePWM (DB, PC, TZ, ET, DC), HRPWM, F28x ADC, COMP, TEMP,
30.10			-
6.11			C2000 Piccolo MCU F28027 LaunchPad Development Kit, CCSv6, F28x Programming, On-Chip Debugging/ Emulation, Firmware
13.11			F28M35 Concerto - ARM Cortex M3 + C28x dual core processor, H52C1 Concerto Experimenter Kit, Concerto programming
20.11			CC13x0 SimpleLink Processor, CC1310 LaunchPad, CC1350 LaunchPad, CC1350 SensorTag
	27.11	28.11	L1 CCSv7 First Project, System Initialization, Interrupt, PIE, CPU WDG, Timer0/1/2, Firmware, Flash memory programming
27.11			Real Time Operating System, TI-RTOS
	4.12	5.12	L2, Generate a PWM waveform
4.12			Communication - the wireless standard (IEEE 802.15.4e/g)
	11.12	12.12	L3 F28M35 Concerto - Dual core programming, first project
11.12			<b>Projekt, TEST</b>
	18.12	19.12	L4 TI-RTOS Basic on CC13x0
18.12			Wireless Sensor Network
25.12			-
1.01	3.01		-
	8.01	9.01	Proj (1)
8.01			Internet of Things
	15.01	16.01	Proj (2)
15.01			Pipelining,
	22.01	23.01	Proj (3)
22.01			Microprocessor vs DSP